

ABSTRACT OF THE DISCLOSURE

An external display device of a refrigerator which includes a microprocessor enabling a serial data transmission/reception between the external display device and a control unit included in the refrigerator. The microprocessor is coupled with a microprocessor included in the control unit by two voltage supply lines and a minimum number of data transmission lines. Data transmission/reception between the two microprocessors is carried out in an asynchronous serial manner while using an appropriate data format so that each microprocessor recognizes the operation condition of the counter microprocessor. Accordingly, it is possible to simplify the configuration of signal lines required between the external display device and control unit, irrespective of the complexity of functions required. By virtue of the simplified signal line configuration, it is possible not only to reduce the costs, but also to improve the workability required in passing signal lines through a hinge hole to couple the external display device to the control unit.

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